

FIG. 1

(image processor configuration)

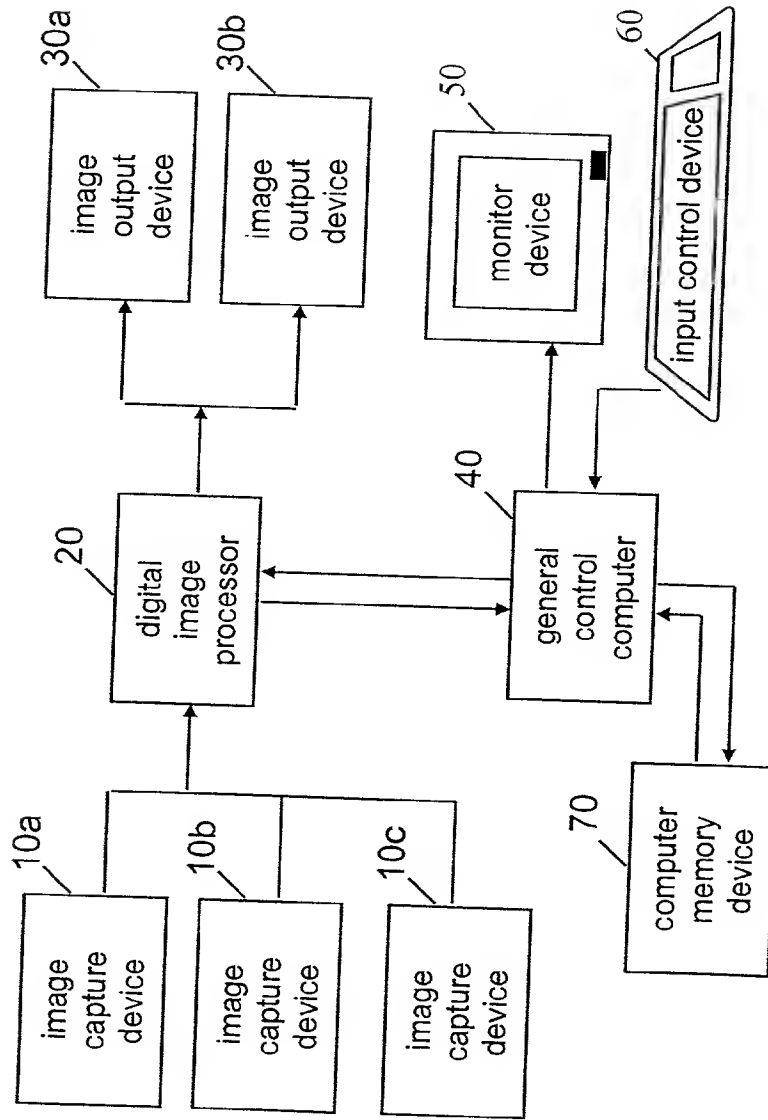


FIG. 2

(image processor)

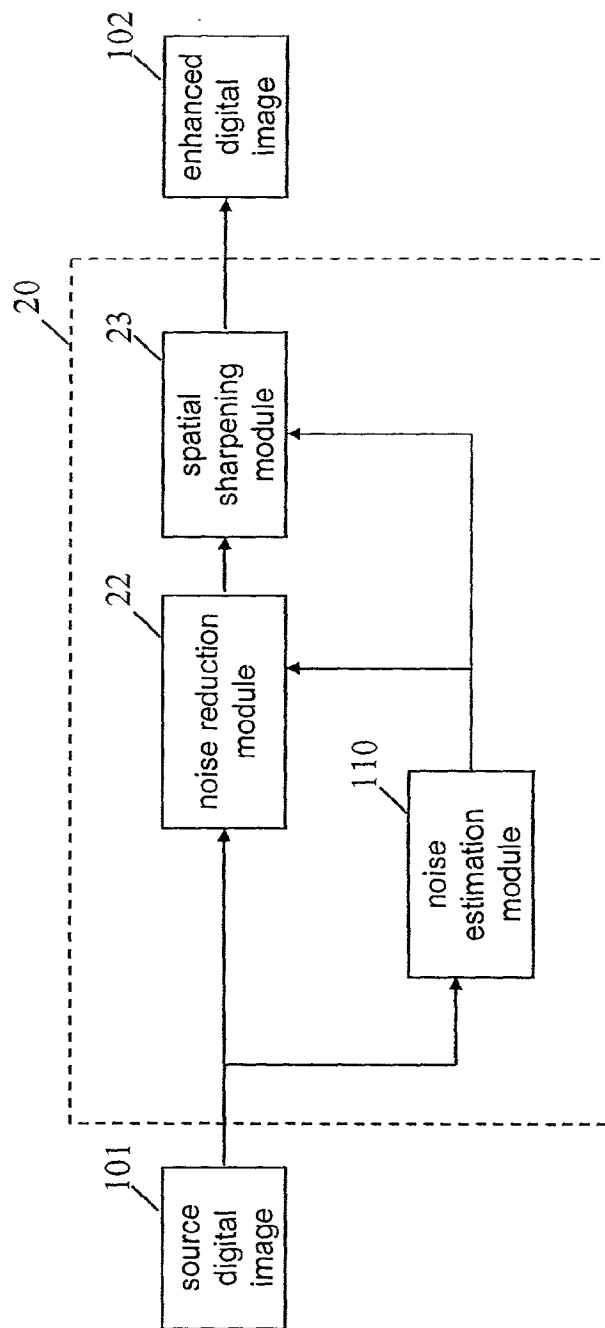


FIG. 3

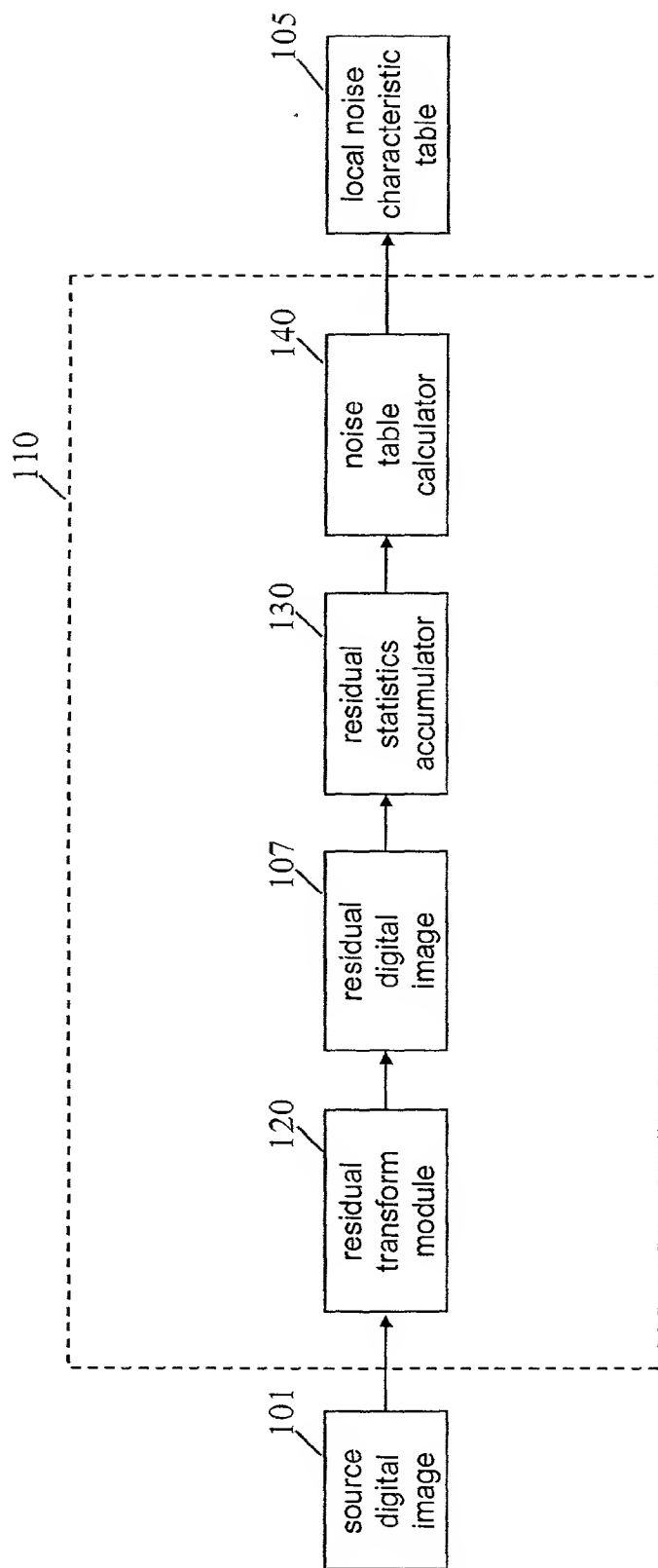


FIG. 4

D		B		C
	D	B	C	
A	A	X	A	A
	C	B	D	
C		B		D